

Chip-scale Photonics Testing Facility Internal User Agreement

This document will serve as an agreement between the Chip-scale Photonics Testing Facility, and _____ ("USER"), regarding the use of the Chip-scale Photonics Testing Facility. The USER hereby acknowledges and agrees to comply with all Chip-scale Photonics Testing Facility Policies and Procedures. The USER further acknowledges to have attended and completed the UC Laboratory Safety Fundamentals training and submitted a copy of the certificate of completion to the Chip-scale Photonics Testing Facility staff, and that the USER has attended the Chip-scale Photonics Testing Facility Orientation and Lab Safety Training and completed the Chip-scale Photonics Testing Facility Policies and Procedures examination. The USER agrees to follow any and all policies pertaining to gaining access to the Chip-scale Photonics Testing Facility, authorization to use any systems or tools in the Chip-scale Photonics Testing Facility, and for performing any experiments in the Nano3 facility according to Chip-scale Photonics Testing Facility Protocol.

The Principal Investigator ("PI") acknowledges responsibility for facility fees incurred by the USER in his/her use of Chip-scale Photonics Testing Facility. The PI acknowledges he/she may be financially liable for equipment or other property damage if it is found to result from gross negligence or deliberate violation, by the USER, of standard Chip-scale Photonics Testing Facility policies and procedures.

While the Chip-scale Photonics Testing Facility will train the USER in general safety before allowing the use of equipment, and in the basic operation of the particular instruments required by USER's work, the USER assumes primary responsibility for his/her personal safety. Any processing involving optical sources in the Chip-scale Photonics Testing Facility must follow the proper laser operating procedure, and the USER must be familiar with all appropriate laser safety information and procedures.

The USER confirms that she/he will obtain special permission from Chip-scale Photonics Testing Facility staff for introduction of any equipment inside the facility. The user also confirms that no biohazards will be introduced into the Chip-scale Photonics Testing Facility.

It is expected that the USER will operate all instruments and equipment in a safe and professional manner, consistent with the operating instructions and the laboratory rules. The USER acknowledges that the Chip-scale Photonics Testing Facility serves a community of professional and student researchers. As such, courteous, professional, responsible behavior is expected at all times. Access to the Chip-scale Photonics Testing Facility is a privilege and may be revoked at any time.

User (print, sign, and date)

Principle Investigator (print, sign, and date)

Chip-scale Photonics Testing Facility (print, sign, and date)